

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	13978	(memory adj cell) and ((peripheral or periphery or logic or drive) adj circuit)	USPAT	OR	ON	2005/04/28 13:58
L3	8818	1 and (groove or recess or trench or via)	USPAT	OR	ON	2005/04/28 13:50
L4	1128	3 and (self adj align\$3)	USPAT	OR	ON	2005/04/28 13:51
L5	1068	4 and transistor	USPAT	OR	ON	2005/04/28 13:51
L6	47	((memory adj cell) with groove) and (((peripheral or periphery or logic or drive) adj circuit) with groove)	USPAT	OR	ON	2005/04/28 13:59
L7	184	((memory adj cell) with (groove or trench or recess)) and (((peripheral or periphery or logic or drive) adj circuit) with (groove or trench or recess))	USPAT	OR	ON	2005/04/28 14:49
L8	1	("6436753").PN.	USPAT	OR	OFF	2005/04/28 15:31
L9	1	8 and peripheral	USPAT	OR	ON	2005/04/28 16:44
L10	97	7 and (self adj align\$4)	USPAT	OR	ON	2005/04/28 16:45
L11	35	7 and ((self adj align\$4) with (groove or recess or trench or via))	USPAT	OR	ON	2005/04/28 17:03
L12	3	(("6436753") or ("6667507") or ("6034416")).PN.	USPAT	OR	OFF	2005/04/28 17:05
L13	1	("6869859").PN.	USPAT	OR	OFF	2005/04/28 17:05
L16	994	438/197.ccis. or 438/299.ccis.	USPAT	OR	ON	2005/04/28 17:06
L17	1224	438/400.ccis. or 438/424.ccis.	USPAT	OR	ON	2005/04/28 17:06
L18	2114	438/424-438.ccis.	USPAT	OR	ON	2005/04/28 17:07
L19	175	18 and (transistor and memory and isolation and (peripheral or logic))	USPAT	OR	ON	2005/04/28 17:08
L20	169	19 and (trench or groove or recess or via)	USPAT	OR	ON	2005/04/28 17:08